

Electronic Acknowledgement Receipt

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| EFS ID: | 6809218 |
| Application Number: | 10801503 |
| International Application Number: | |
| Confirmation Number: | 1822 |
| Title of Invention: | Fractional-type Phase-Locked Loop circuit with compensation of phase errors |
| First Named Inventor/Applicant Name: | Guido Gabriele Albasini |
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| Filer: | Kevin Duane Jablonski/Cindy McKee |
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| Application Type: | Utility under 35 USC 111(a) |

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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) | | | | |
|-------------------------------------|--|---------------------------------------|---|------------------|------------------|--|--|--|--|
| 1 | | 2110-111-03_20091104_OAR_20100114.pdf | 172739 8f7b3213f74b95018808c5c17ed583d7e9c56e | yes | 16 | | | | |
| | Multipart Description/PDF files in .zip description | | | | | | | | |
| | Document Description | | Start | End | | | | | |
| | Amendment/Req. Reconsideration-After Non-Final Reject | | 1 | 1 | | | | | |
| | Claims | | 2 | 10 | | | | | |
| | Applicant Arguments/Remarks Made in an Amendment | | 11 | 16 | | | | | |
| Warnings: | | | | | | | | | |
| Information: | | | | | | | | | |
| 2 | Fee Worksheet (PTO-875) | fee-info.pdf | 29928 2635989ab779c7648ea3b14b073e735f6bc6a7ff | no | 2 | | | | |
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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